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RESPONSE
2-17-04
Mallish

THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Ramsbey, et al.

Serial No.: 09/430,366 ✓

Filed: October 28, 1999 ✓

For: Method Of Making A Memory Cell
With Polished Insulator Layer

Art Unit: 2813

Examiner: Chen, Jack S. J.

RESPONSE TO FINAL OFFICE ACTION

✓
Mail Stop AF
Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir/Madam:

This is in response to the *Final Office Action* dated November 14, 2003 in the
above-referenced patent application. Please enter and consider the following remarks.

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TECHNOLOGY CENTER 2800